



FIG. 1

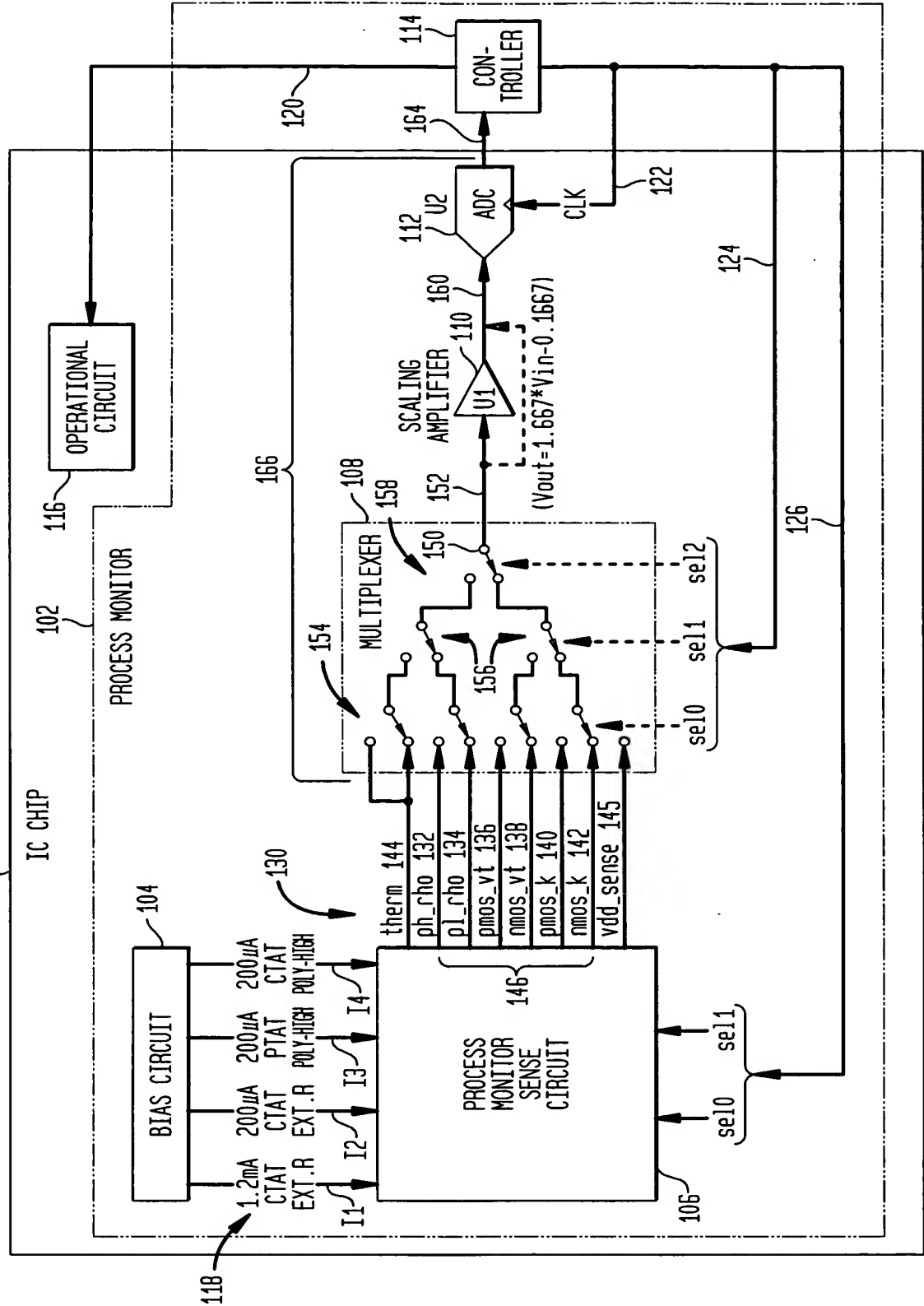


FIG. 1A

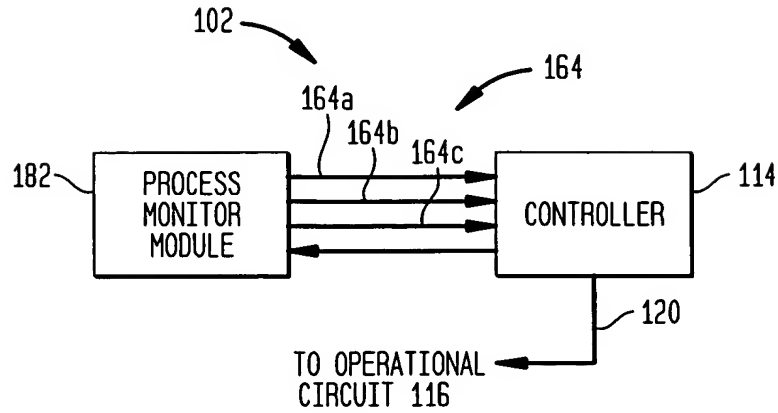


FIG. 2A

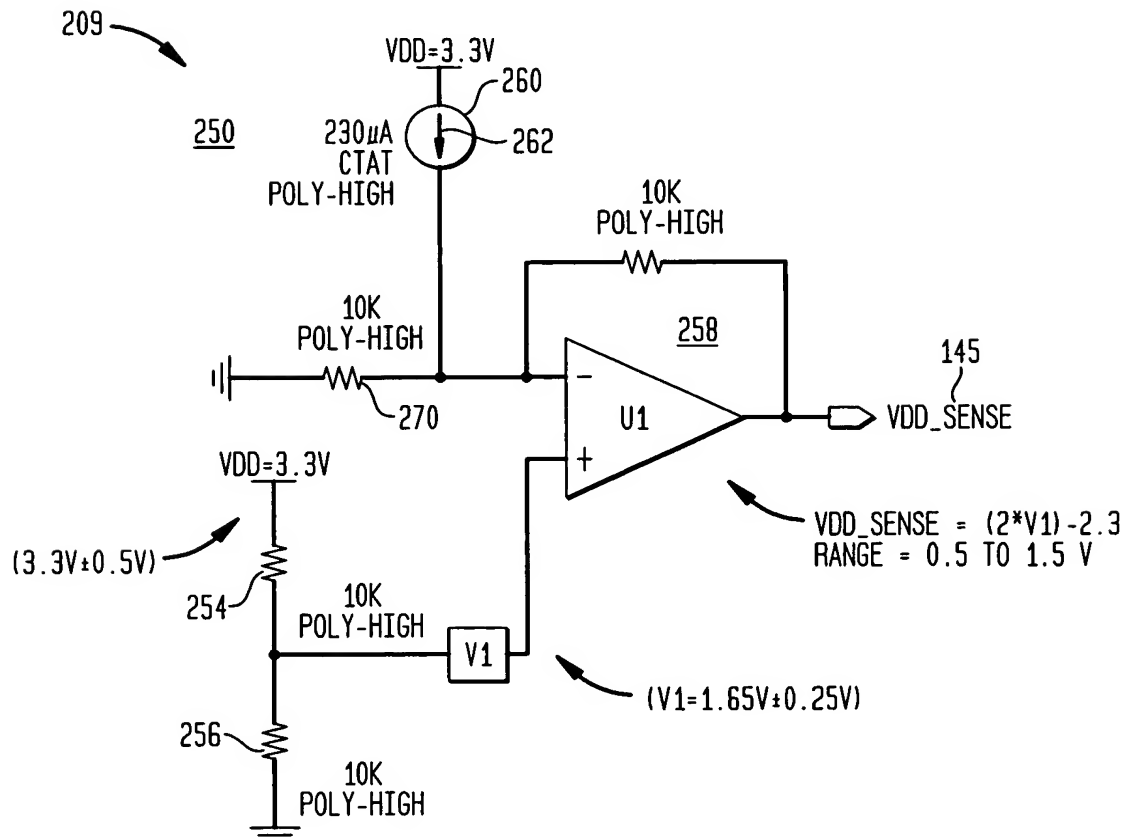


FIG. 2

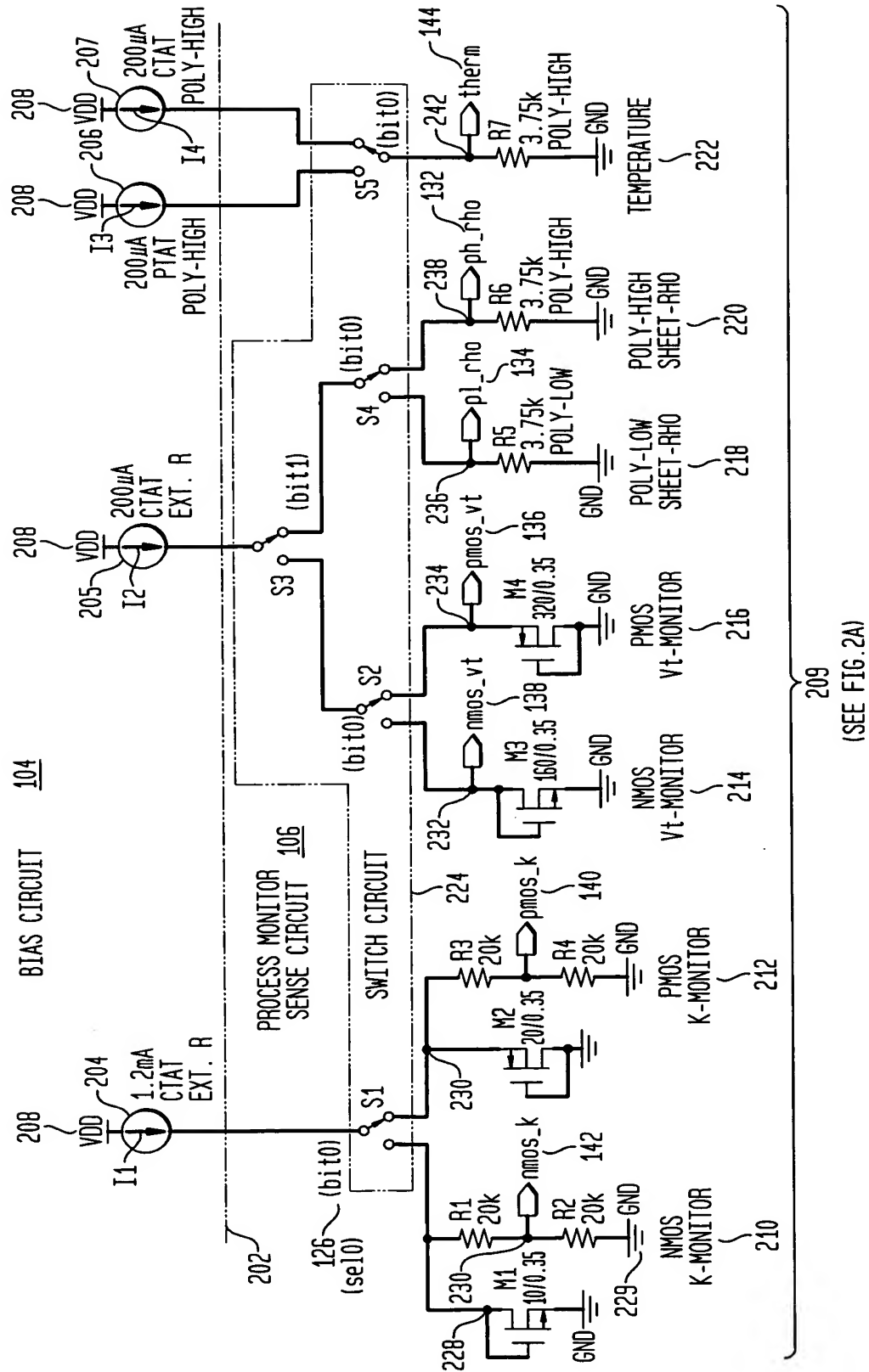


FIG. 2B

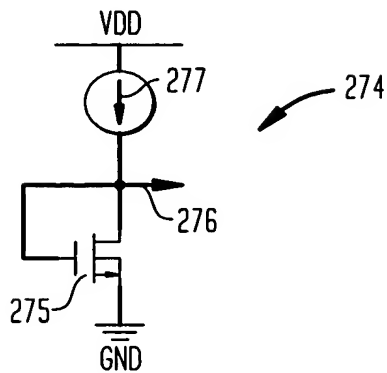


FIG. 3

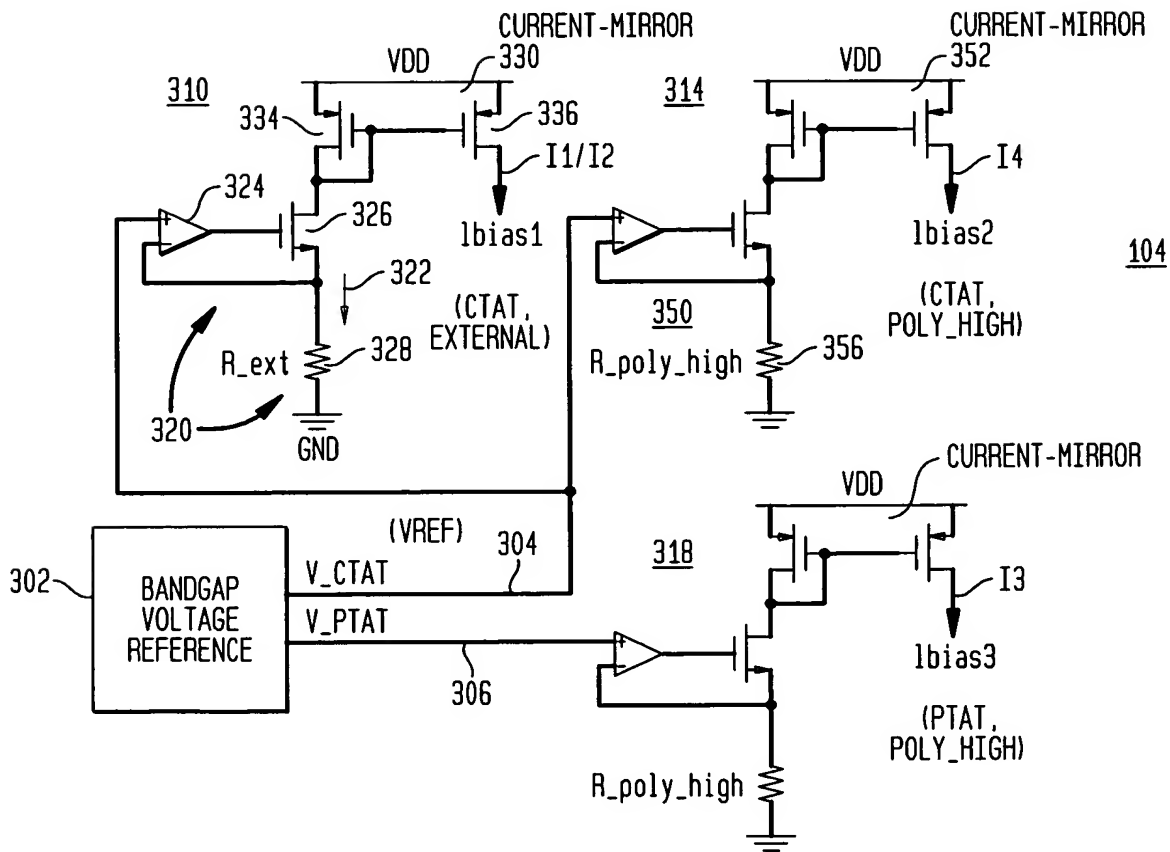


FIG. 4

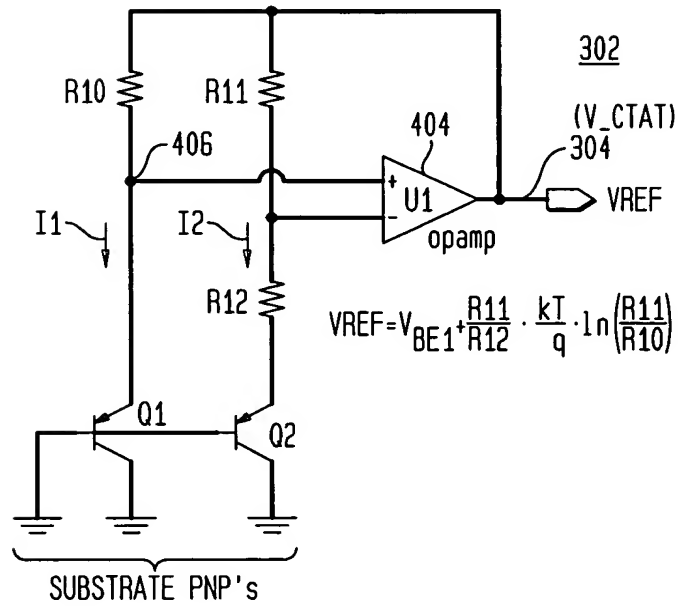
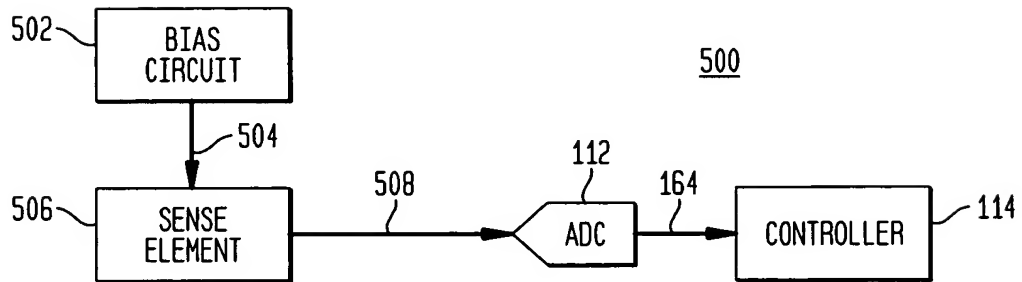


FIG. 5



Appl. No. 10/647,472; Filed: August 26, 2003
Dkt. No. 1875.3770001; Group Art Unit: 2829
Inventors: Burns *et al.*; Tel: 202/371-2600
Title: Process Monitor For Monitoring An Integrated
Circuit Chip

FIG. 6
OPERATIONAL CIRCUIT

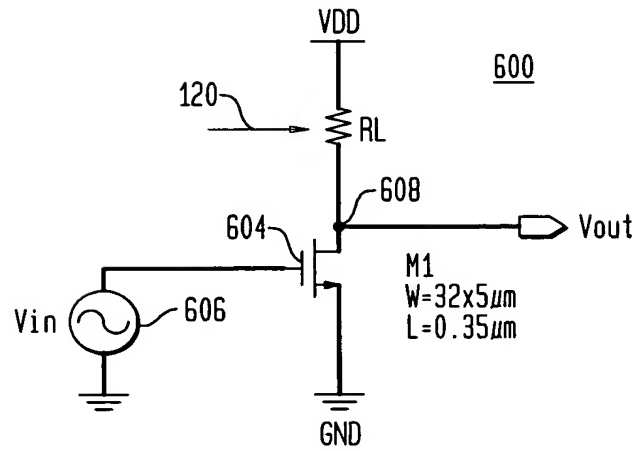


FIG. 7

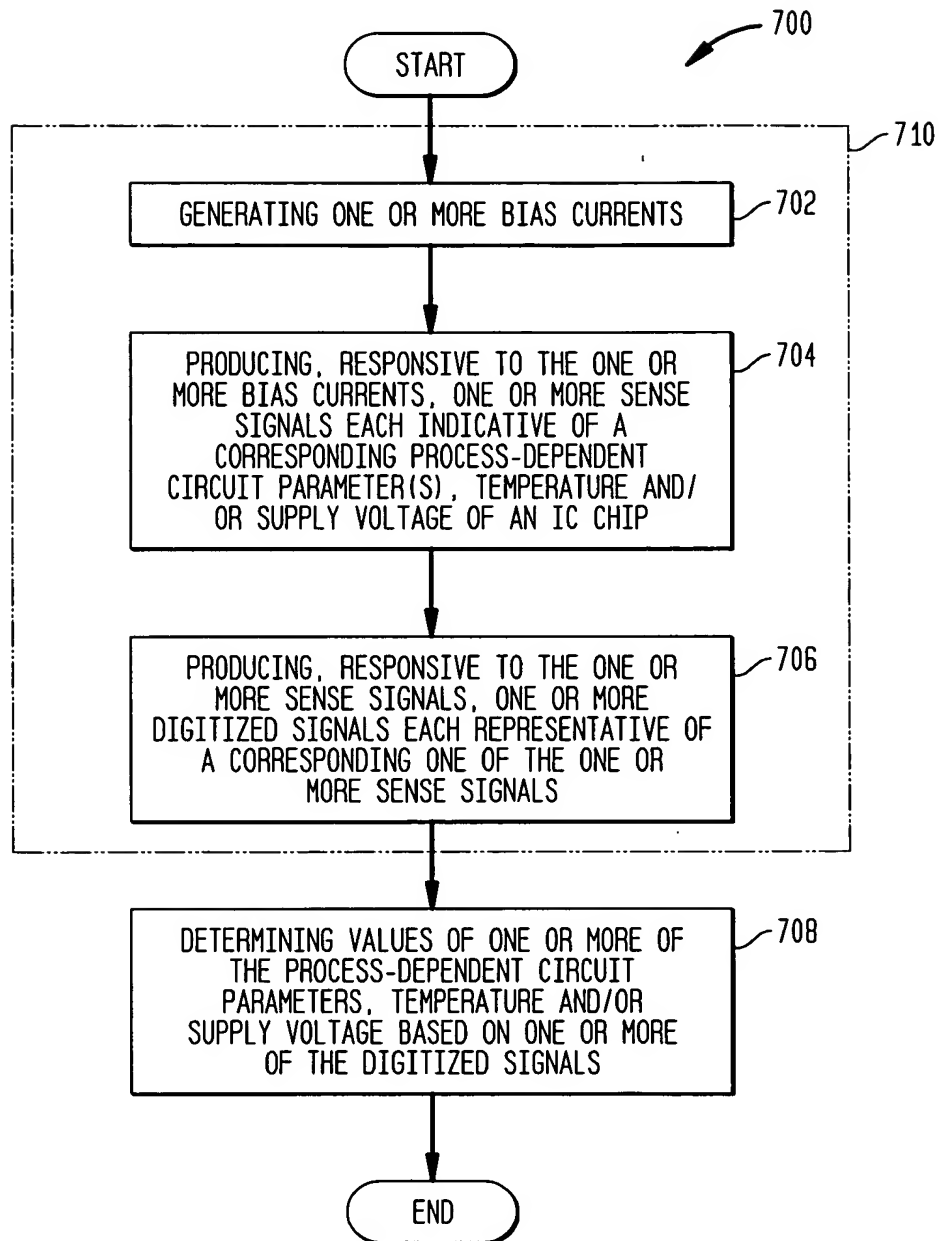


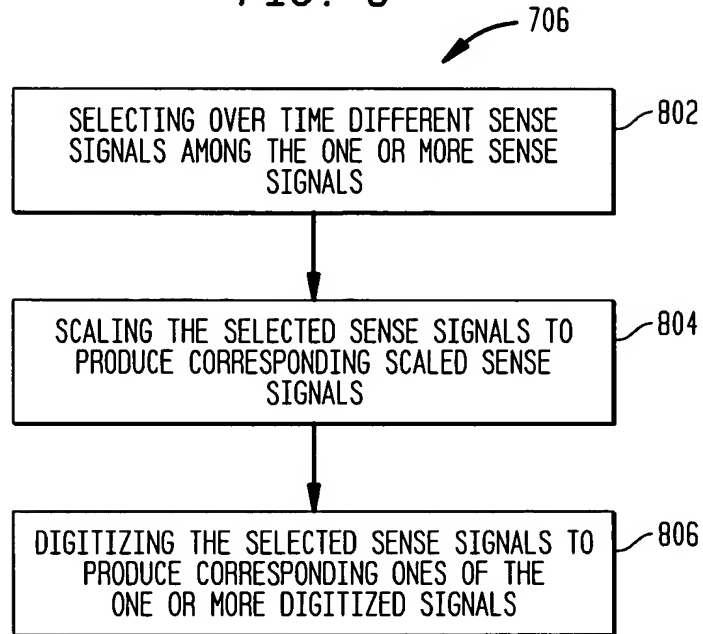
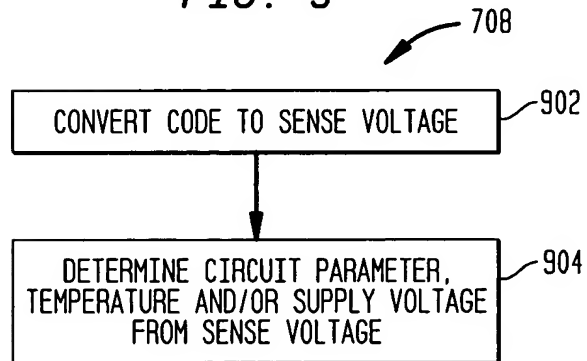
FIG. 8**FIG. 9**



FIG. 10

